

**IN THE SPECIFICATION**

Pursuant to the request of the Examiner and 37 C.F.R. § 1.1.25(a), Applicant respectfully requests amendments to the specification as follows:

Please amend the paragraph beginning on page 1, line 1 as follows:

A1  
**RELATED APPLICATIONS**

This application is related to United States patent application entitled "*Server Chassis Hardware Master System and Method*" filed on May 4, 2001 under Serial No. 09/848,816 (Attorney's Docket 067856.0213).

Please amend the paragraph beginning on page 3, line 18 as follows:

A2  
According to one aspect of the present invention, a method for monitoring a plurality of server processing cards of a server chassis includes selecting at least one hardware master from among a plurality of server processing cards. A hardware master control signal may be transmitted to the hardware master. A hardware master module coupled with the hardware master may activate in response to the master control signal. The hardware master may be operable to monitor operating information regarding at least a subset of the plurality of server processing cards. In particular embodiments, the operating information may include health and/or ~~an/or~~ configuration data associated with the subset of the plurality of server processing cards.

Please amend the paragraph beginning on page 18, line 16 as follows:

A3  
The hardware master selected from network interface card 51 and server processing cards 32 and 33 controls components of server chassis 30 using command bus 80, control bus 82, and/or ~~and~~ I<sup>2</sup>c bus 83. In a particular embodiment, command bus 80 includes an RS-485 bus. The hardware master includes the ability to perform remote resets (e.g., reboot) of any particular server processing card using the control bus 82.

Please amend the paragraph beginning on page 21, line 8 as follows:

A4  
In one embodiment, server processing card 32 includes a powerful computer connected to the Internet and operable to store audio, video, data graphics and/or text files in order to display to a user of a public network 46 via protocols including, without limitation, hypertext transfer protocol (HTTP). Each server processing card 32 includes a printed circuit board 120, coupled with a central processing unit (CPU) 122, a disk drive 124, a dynamic memory integrated circuit 93, and network interface integrated circuitry 128-130.